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PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10032757	FILING DATE 12/27/2001	CLASS 257	SUBCLASS 3	GAU 2811	EXAMINER
<b>**APPLICANTS:</b> Hui Angela; Fang Shenqing; Kinoshita Hiroyuki; Ko Kelwin; Li Wenmei; Sun Yu; Ogawa Hiroyuki; Chang Chi;					
<b>**CONTINUING DATA VERIFIED:</b>					
<b>** FOREIGN APPLICATIONS VERIFIED:</b>					
PG-PUB <input type="checkbox"/>		DO NOT PUBLISH <input checked="" type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's initials				ATTORNEY DOCKET NO G0728/2236P	
TITLE : Method and system for forming dual gate structures in a nonvolatile memory using a protective layer					

U.S. DEPT. OF COMM. / PAT. & TM. PTO-436L/Rev. 12-94

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>Assistant Examiner</b>		<b>CLAIMS ALLOWED</b>	
				Total Claims	Print Claim for O.G.
<b>ISSUE FEE</b>		<b>Primary Examiner</b>		<b>DRAWING</b>	
Amount Due	Date Paid			Sheets Drwg.	Figs. Drwg.
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		<b>PREPARED FOR ISSUE</b>		<b>Application Examiner</b>	
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